

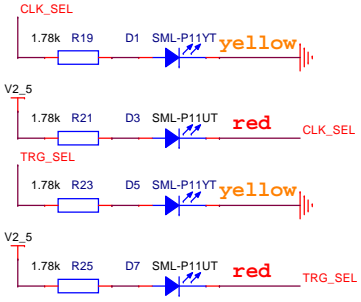
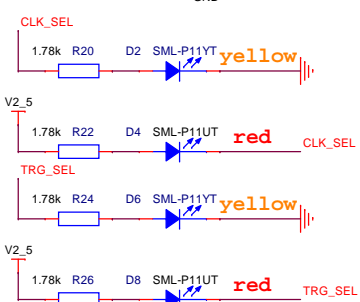
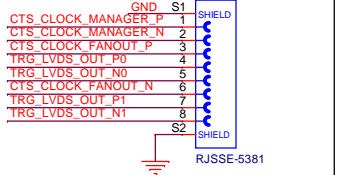
**J1**  
 1 CTS\_CLOCK\_MANAGER\_P  
 2 CTS\_CLOCK\_MANAGER\_N

**J2**  
 1 CTS\_CLOCK\_FANOUT\_P  
 2 CTS\_CLOCK\_FANOUT\_N

**J3**  
 1 TRG\_LVDS\_IN\_P0  
 2 TRG\_LVDS\_IN\_N0  
 3 TRG\_LVDS\_IN\_P1  
 4 TRG\_LVDS\_IN\_N1

**J4**  
 1 TRG\_LVDS\_IN\_P1  
 2 TRG\_LVDS\_IN\_N1

Indicate the signal polarities on the PCB with info J3: Trigger J4: Trigger 2



Please place the LEDs by the info text  
 Clock Source LEMO: D4 RJ45: D2  
 Trigger Source CTS : D8 Daisy: D6

Please place the LEDs by the CTS\_TRIGGER\_INP1 connector

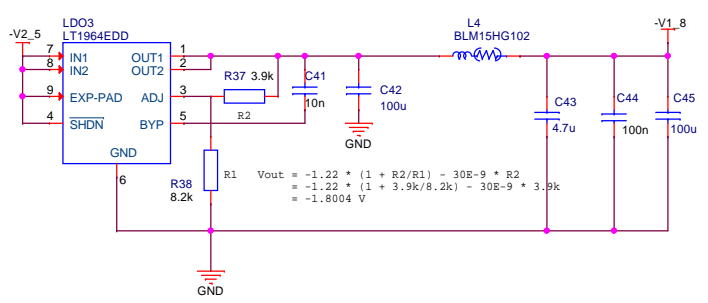
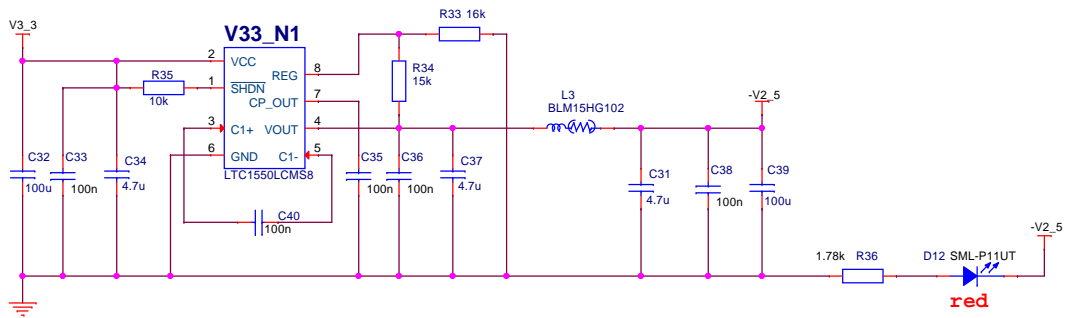
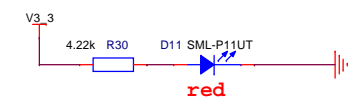
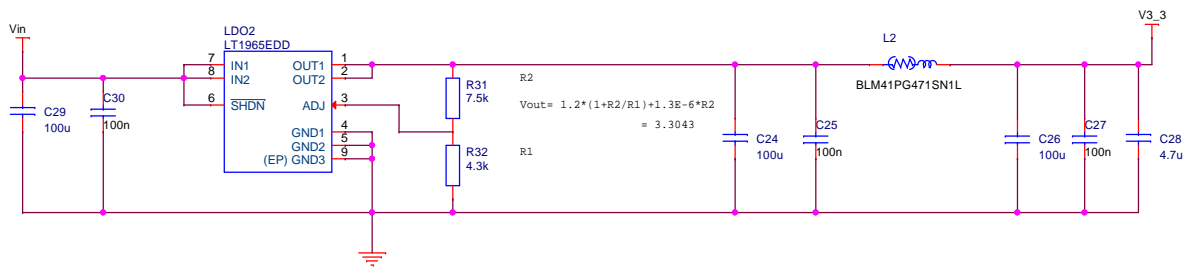
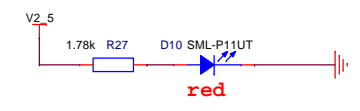
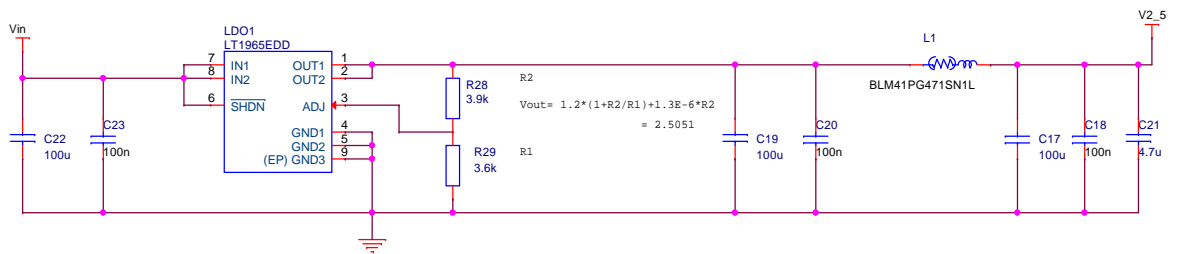
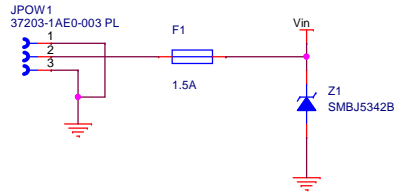
# GSI

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 D-64291 Darmstadt  
 GERMANY  
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**TRBDIST3** Trigger

Design: K:\GSI\JOB\HADES\TRBV3\TRBDIST3\TRBDIST3.DSN	Size: A3	Page: 1 / 2
Modified: Friday, November 18, 2016	Layouter: H.Kayan	



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TRBDIST3 POWER

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Design: K:\GSI\JOB\HADES\TRBV3\TRBDIST3\TRBDIST3.DSN	Size: A3	Page: 2 / 2
Modified: Friday, November 18, 2016	Layouter: H.Kayan	
Designer: M.Traxler		